SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	MICRTOCONTROLLERSAND PROGRAMMABLE DSP
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-I
Regulation:	R19
Subject Code:	**M5502**
Name of the Faculty:	Mr. Ch. Kutumba Rao

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Compare the ARM processor core based SoC with several features/peripherals	Apply
CO 2	Identify and characterize architecture of Programmable DSP Processors	Understand
CO 3	Develop small applications by utilizing the ARM processor core and DSP processor-based platform.	Understand
CO 4	Analyze discrete-time signals and systems in various domains	Analyze
CO 5	Verify the functionality of the VLIW architecture and TMS320C6000 series	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	RTL SIMULATIONAND SYNTHESISWITH PLDS
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-I
Regulation:	R19
Subject Code:	**M6801**
Name of the Faculty:	Dr.B. B .M Krishna kanth

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Obtain the Verilog HDL to design a digital circuit.	Apply
CO 2	Obtain the analysis of finite state machine of a controlling circuit	Understand
CO 3	Understand the Static Timing Analysis and clock issues in digital circuits	Understand
CO 4	Verify the functionality of the digital designs using PLDs	Analyze
CO 5	Verify the functionality of Configurable PAL‟s,	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	DIGITAL SIGNAL AND IMAGE PROCESSING
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-I
Regulation:	R19
Subject Code:	**M5503**
Name of the Faculty:	Ms. T. Lakshmi Prasanna

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Analyze Characterization of time view of Discrete Time signals and systems	Apply
CO 2	Design the filters (both IIR and FIR) from the given specifications	Understand
CO 3	Obtain the quantization effects in digital filters and understand the basics of image sampling, quantization and image transforms.	Understand
CO 4	Understand the concepts of frequency domain filters	Analyze
CO 5	Understand the various methods involved in image compression and fundamentals in color image processing.	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	PROGRAMMING LANGUAGES FOR EMBEDDED SYSTEMS
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-I
Regulation:	R19
Subject Code:	**M5506**
Name of the Faculty:	Mr.M. Vara prasad

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Write Embedded „C“ Programming	Apply
CO 2	Develop and analyze algorithms in C++.	Understand
CO 3	Differentiate interpreted languages from compiled languages	Understand
CO 4	Understand the concepts of Overloading and Inheritance	Analyze
CO 5	Understand the concepts of Function template and class template	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	RESEARCH METHODOLOGY AND IPR
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-I
Regulation:	R19
Subject Code:	**M0109**
Name of the Faculty:	Mr.K. Venkateswarlu

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Understand the Sources of research problem	Apply
CO 2	Analyze research related information and Format of research proposal	Understand
CO 3	Understanding that when IPR would take such important place in growth of individuals &nation.	Understand
CO 4	Understand that IPR protection provides an incentive to inventors for further research work and investment in R & D,	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	RTL SIMULATION AND SYNTHESIS WITH PLDS LAB
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-I
Regulation:	R19
Subject Code:	**M6802**
Name of the Faculty:	Dr.B. B .M Krishna kanth

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Solve and implement problems in signal processing, communication Systems etc using RTL design tools.	Apply
CO 2	Solve the problems by EDA tools like Cadence, Mentor Graphics and Xilinx	Understand

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	MICROCONTROLLERS AND PROGRAMMABLE DSP LAB
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-I
Regulation:	R19
Subject Code:	**M6803**
Name of the Faculty:	Mr. Ch. Kutumba Rao

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Configure and utilize tool sets for developing applications based on ARM processor	Apply
CO 2	Design Core SoC and DSP processor	Understand
CO 3	Develop prototype codes using commonly available on and off chip peripherals	Understand
CO 4	Design Cortex M3 and DSP development boards	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	NETWORK SECURITY & CRYPTOGRAPHY
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-II
Regulation:	R19
Subject Code:	**N5507**
Name of the Faculty:	Dr.A.Shravan Kumar

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Identify and utilize different forms of cryptography techniques and OSI Security Architecture,	Apply
CO 2	Incorporate authentication and security in the network application sand Extended Euclidean Algorithm	Apply
CO 3	Distinguish among different types of threats to the system and handle the same.	Understand

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	REAL TIME OPERATING SYSTEMS
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-II
Regulation:	R19
Subject Code:	**N5502**
Name of the Faculty:	Mr.CH.Kutumbarao

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Illustrate real time programming concepts	Apply
CO 2	Apply RTOS functions to implement embedded applications	Apply
CO 3	Understand fundamentals of design consideration for embedded applications	Understand
CO 4	Understand fundamentals of design Real-Time clocks and System Clocks	Analyze
CO 5	Apply RTOS functions to implement embedded Non-Blocking Memory Functions	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	LOW POWER VLSI DESIGN
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-II
Regulation:	R19
Subject Code:	**N6802**
Name of the Faculty:	Mr.K. Venkateswarlu

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Understand the basics of MOS transistors and also the characteristics of MOS transistors and Modes of Operation of MOS Transistors	Apply
CO 2	Understand the MOS fabrication process and short channel effects and CMOS Fabrication Steps.	Apply
CO 3	Learn about the basic rules in layout designing and CMOS Process Enhancements.	Understand
CO 4	Analyse various combinational logic networks and sequential systems	Analyze
CO 5	Solve the problems for optimal solutions in the area of digital ICs	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	ANALOG AND DIGITAL CMOS VLSI DESIGN
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-II
Regulation:	R19
Subject Code:	**N6801**
Name of the Faculty:	Ms.B. Rajya Lakshmi

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Appreciate the trade-offs involved in analog integrated circuit design.	Apply
CO 2	Understand and appreciate the importance of distortion in analog circuits	Apply
CO 3	Analyze complex problems critically in the domain of analog IC design for conducting research.	Understand
CO 4	Demonstrate advanced knowledge in Static and dynamic characteristics of CMOS,	Analyze
CO 5	Solve the problems for optimal solutions in the core area of digital ICs	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	ANALOG AND DIGITAL CMOS VLSI DESIGN LAB
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-II
Regulation:	R19
Subject Code:	**N6803**
Name of the Faculty:	Dr.A. Shravan Kumar

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Understand the basics of MOS transistors and also the characteristics of MOS transistors and Modes of Operation of MOS Transistors	Apply
CO 2	Understand the MOS fabrication process and short channel effects and CMOS Fabrication Steps.	Apply
CO 3	Learn about the basic rules in layout designing and CMOS Process Enhancements.	Understand
CO 4	Analyse various combinational logic networks and sequential systems	Analyze
CO 5	Solve the problems for optimal solutions in the area of digital ICs	Analyze

SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY: TIRUVURU

Department OF ECE

Course Outcomes

Course Title:	REAL TIME OPERATING SYSTEMS LAB
Programme:	M. Tech (VLSI&ES)
Academic Year	2019-20
Year/Semester:	I-II
Regulation:	R19
Subject Code:	**N5504**
Name of the Faculty:	Mr.CH.Kutumbarao

COURSE OUTCOMES(COs): Upon completion of the course, students will be able to:

S.No.	Course Outcomes	Blooms Taxonomy level
CO1	Understand the basics of Experiments using ARM-926 with PERFECT RTOS	Apply
CO 2	Understand the Experiments on ARM-CORTEX processor using any open source RTOS.	Apply
CO 3	Learn about the basic rules LINUX Environment for the compilation using Eclipse IDE & Java with latest version.	Understand